



**DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING  
NATIONAL INSTITUTE OF TECHNOLOGY, SILCHAR**

Date: 08/05/2018.

**List of Candidates Selected provisionally for 6-weeks Summer Internship on  
VLSI Circuit Design (organised by ECE Dept.)**

The following students have been selected provisionally for attending the 6-weeks Summer Internship on VLSI design in the Dept. of ECE, NIT Silchar from 1<sup>st</sup> June, 2018.

Sl. No.	Name	Institute
1	Sagar Suman	School of Engineering, Cochin University of Science & Technology
2	V.Manikandan	SNS College of Technology, Coimbatore
3	Debasmita Dey	Sikkim Manipal Institute of Technology
4	Debasmita Das	Manipal University Jaipur
5	Rima Sarkar	Jalpaiguri Govt. Engineering College
6	Santu Patra	Jalpaiguri Govt. Engineering College
7	Rituparna Saha	GIMT Guwahati
8	Smita Paul	GIMT Guwahati
9	Tanay Jyoti Das	GIMT Guwahati
10	Syeda Mowsuda Manbi	GIMT Guwahati
11	Arindam Dey Sarkar	NIT Arunachal Pradesh
12	Pankaj Saksena	NIT Arunachal Pradesh
13	Shirija Saha	NIT Arunachal Pradesh
14	Abhishek Dutta	Assam University Silchar

Candidates are to report on 1st of June, 2018 in the Dept. of ECE @ VLSI Lab.

Sd/-

**Dr. K. L. Baishnab  
ECE Dept.**